

FIG. 1

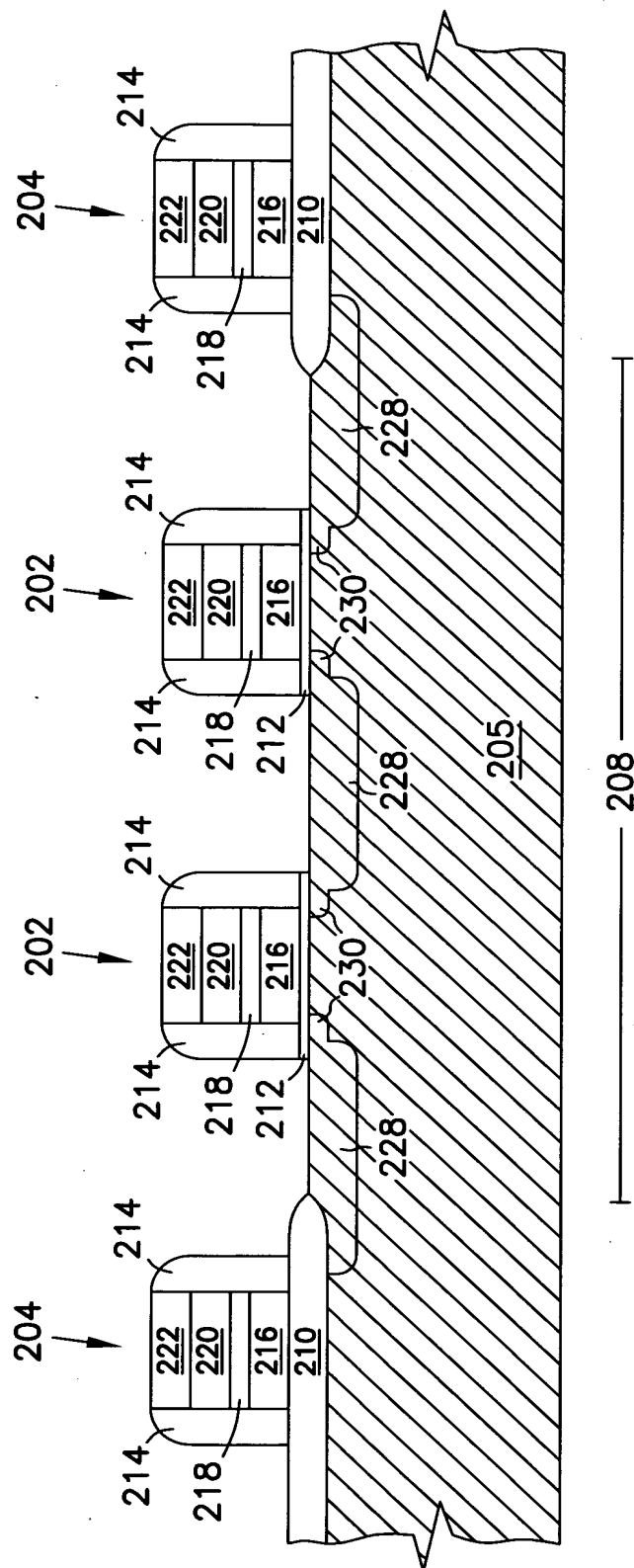


FIG. 2A

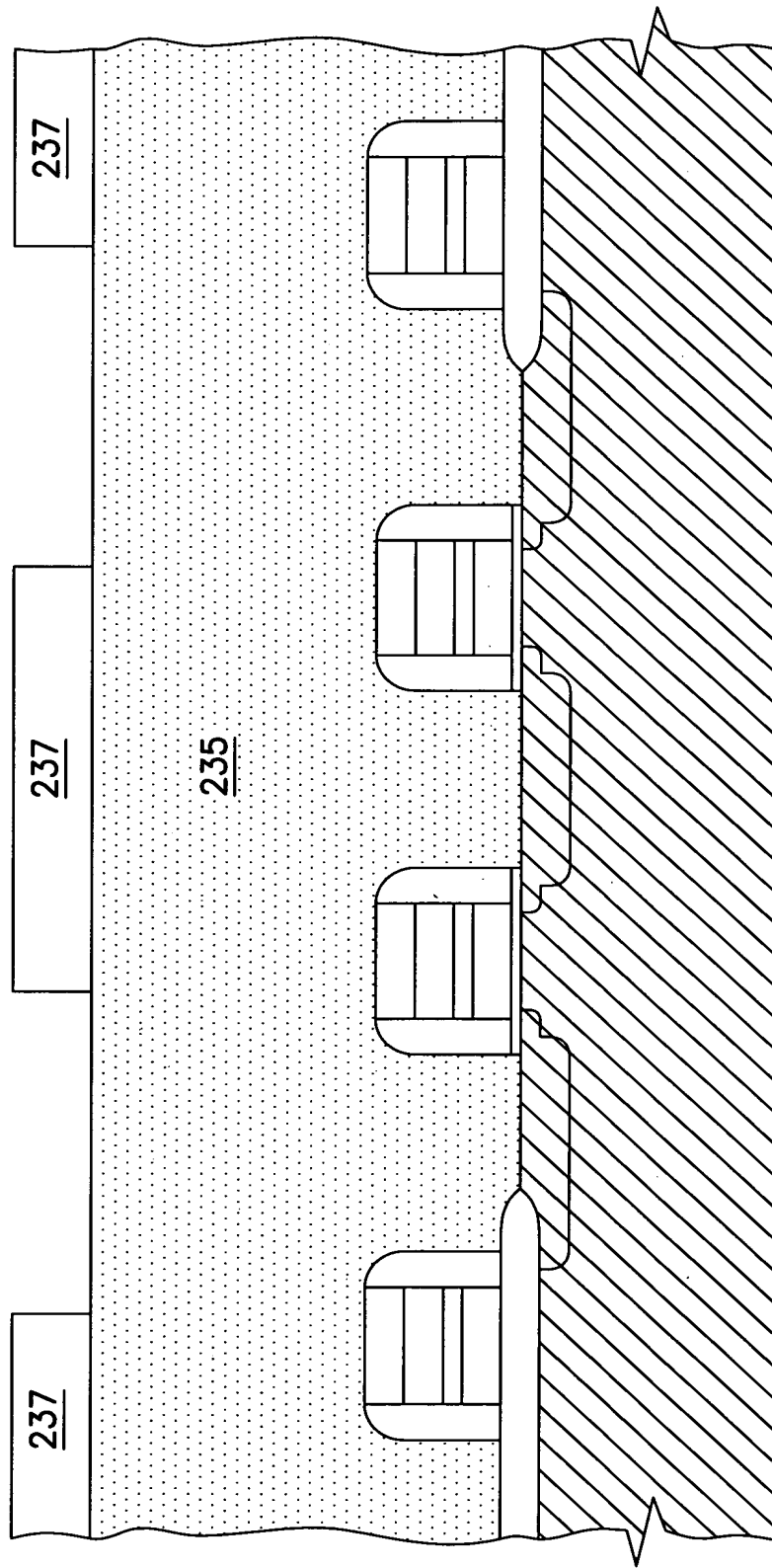


FIG. 2B

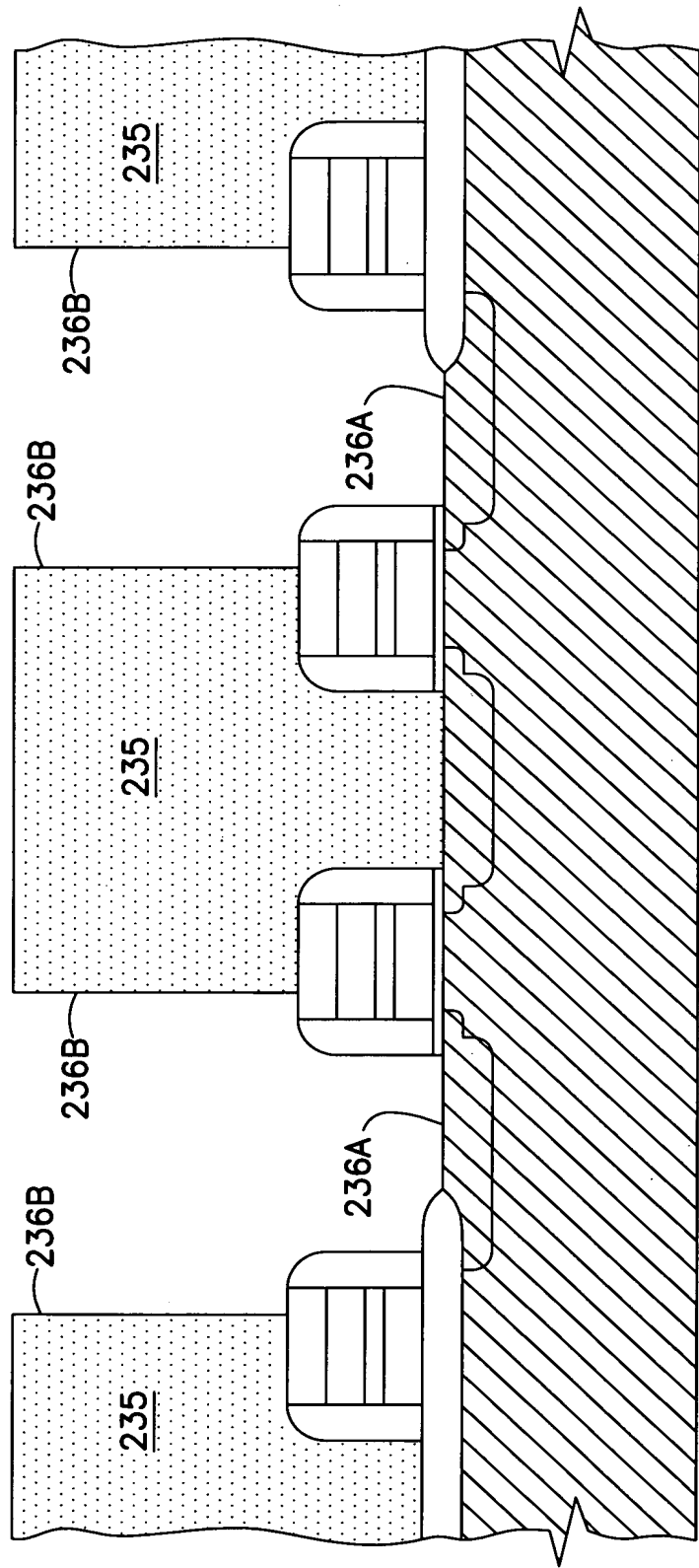


FIG. 2C

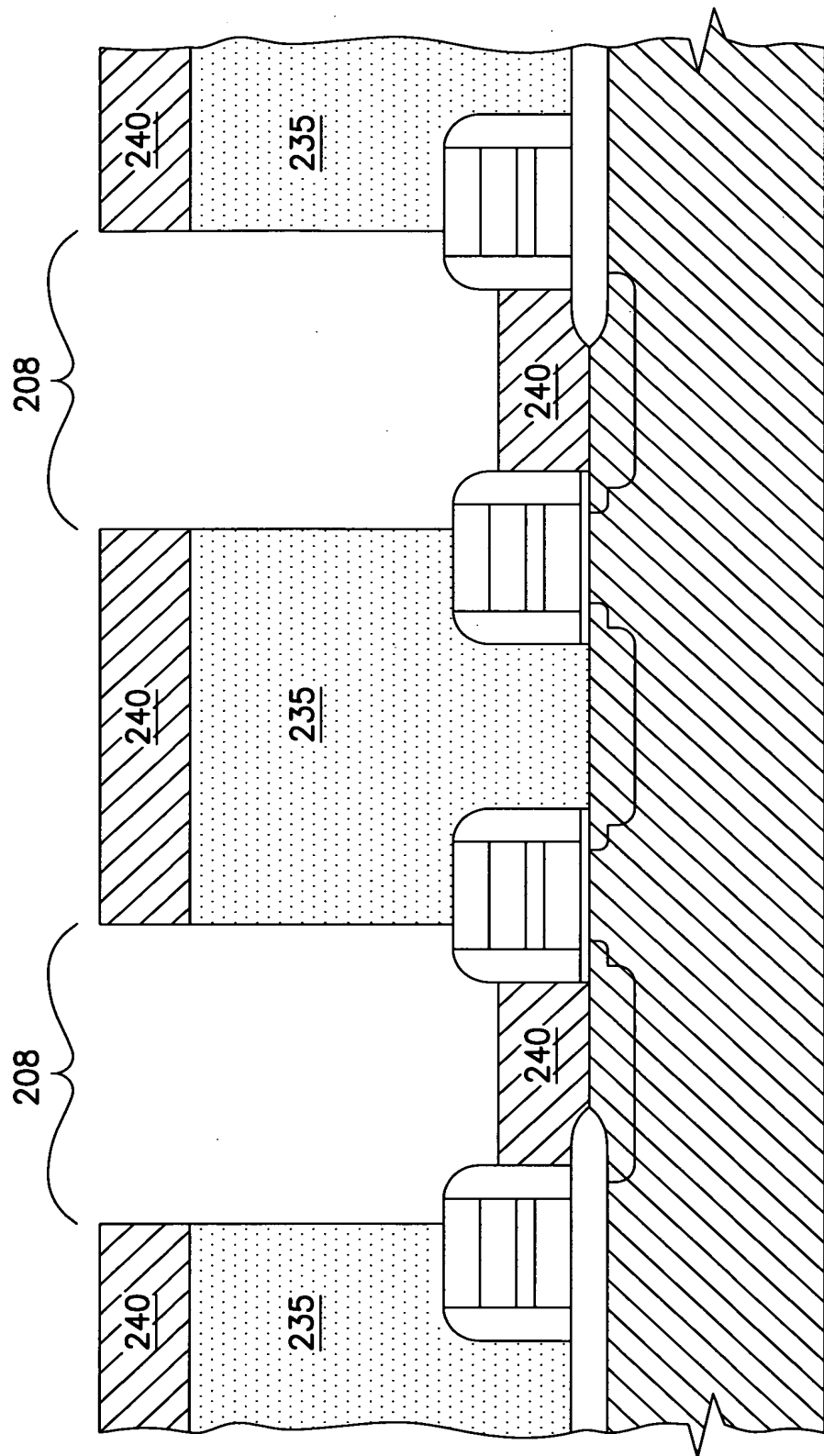


FIG. 2D

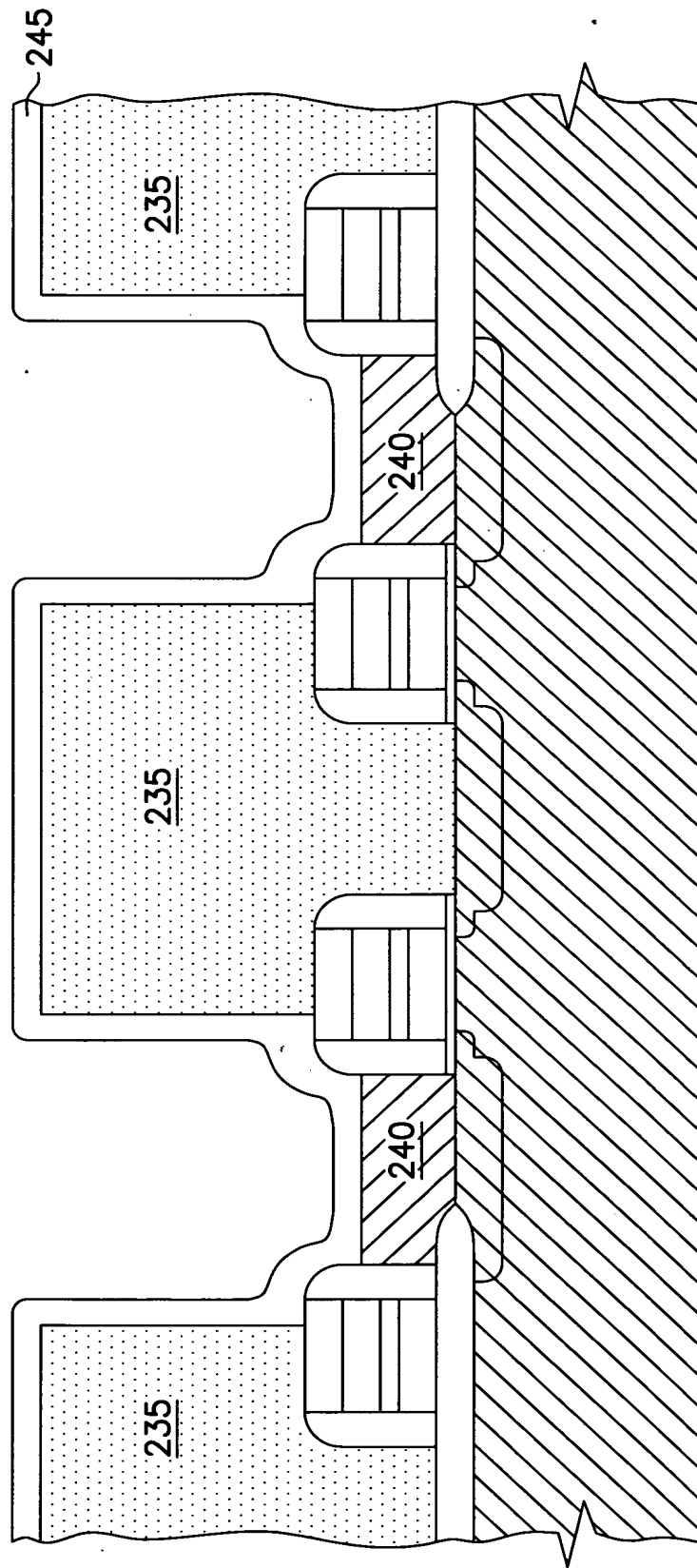


FIG. 2E

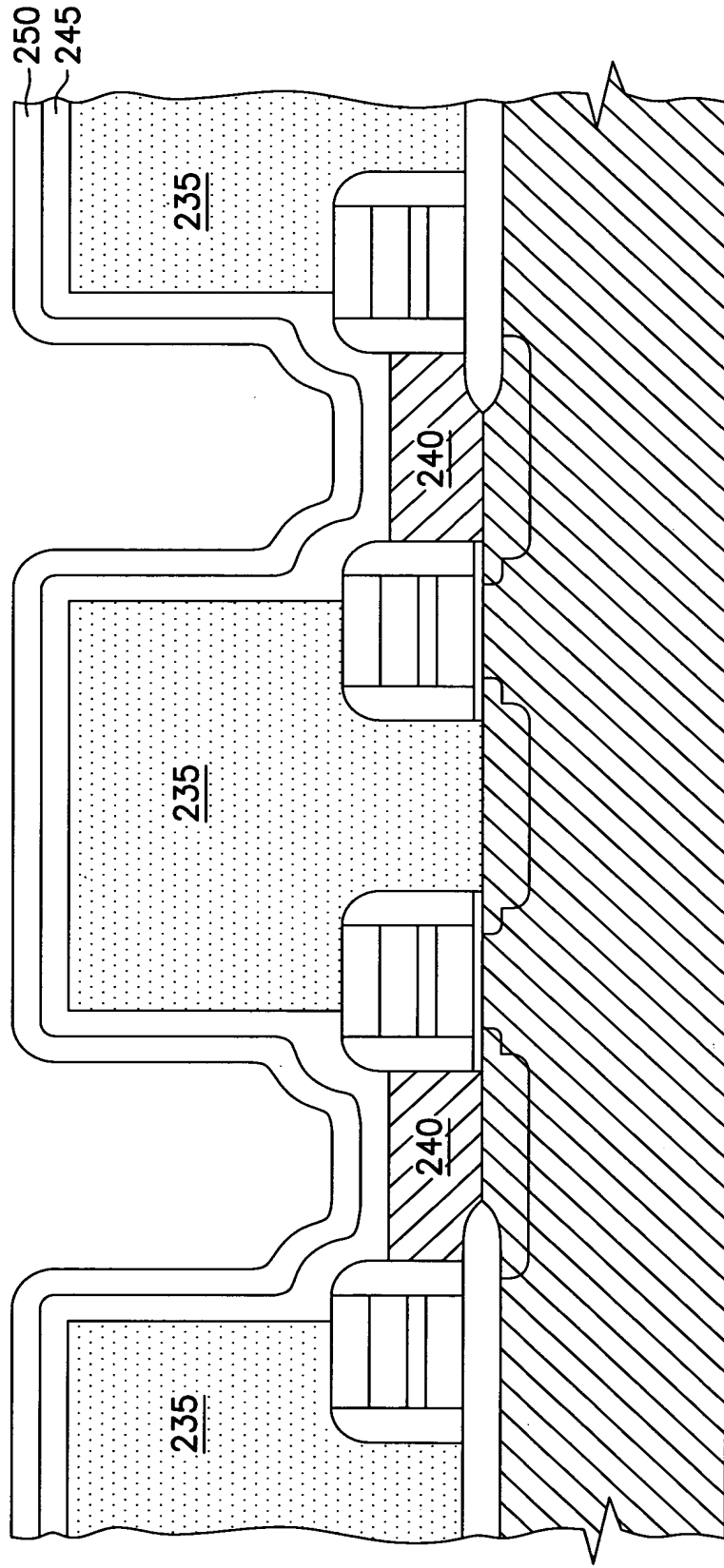


FIG. 2F

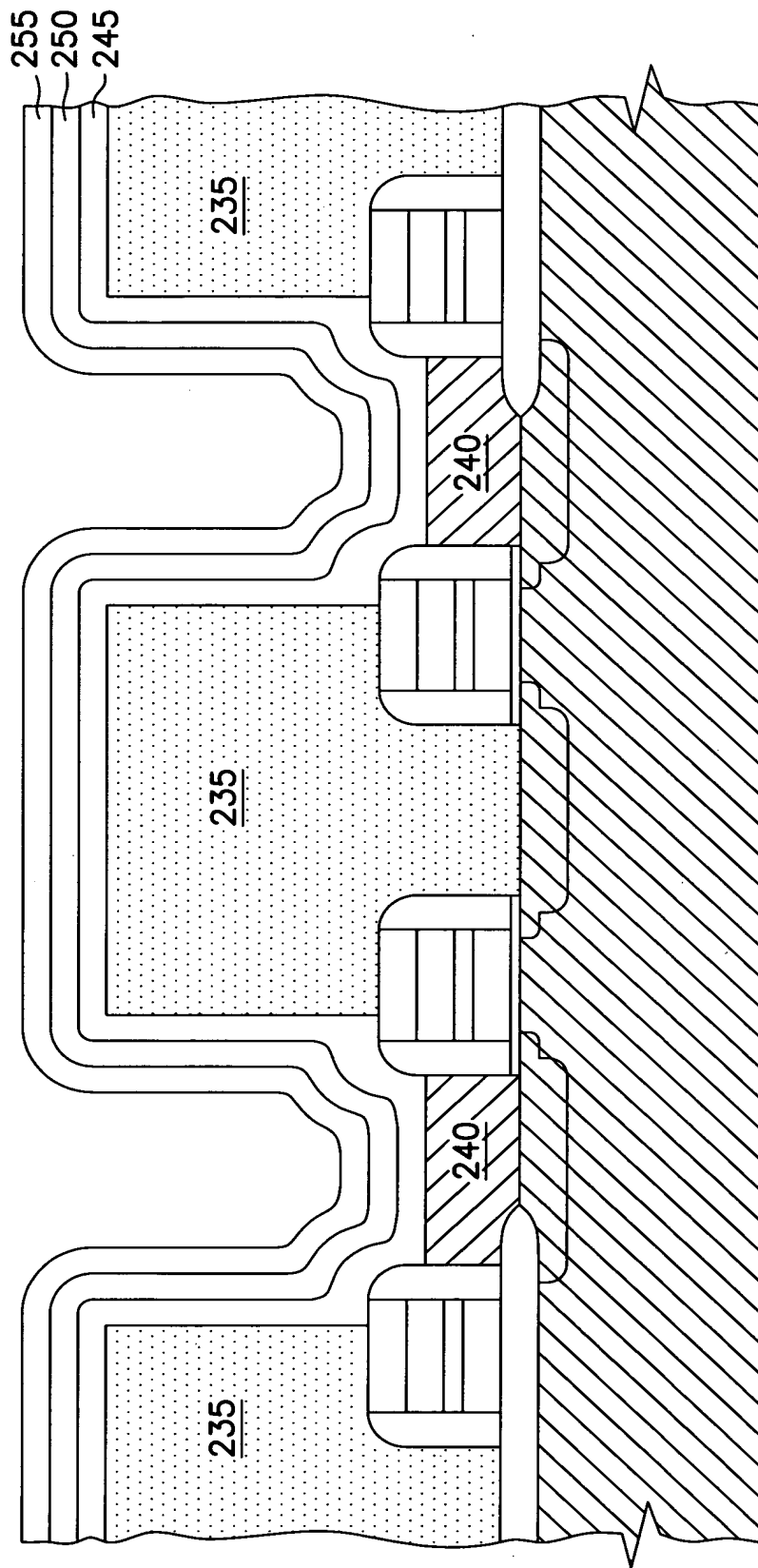


FIG. 2G

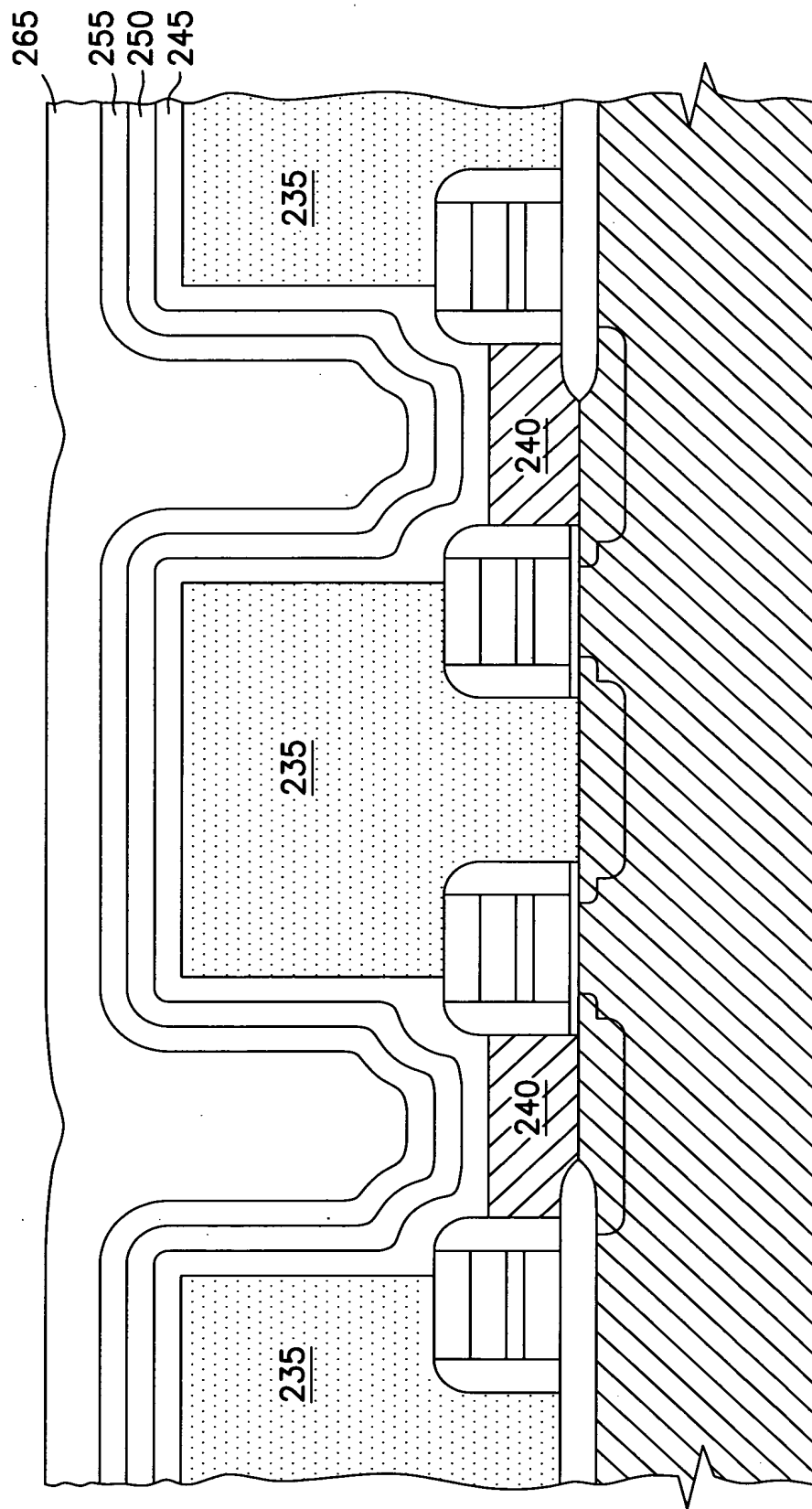


FIG. 2H

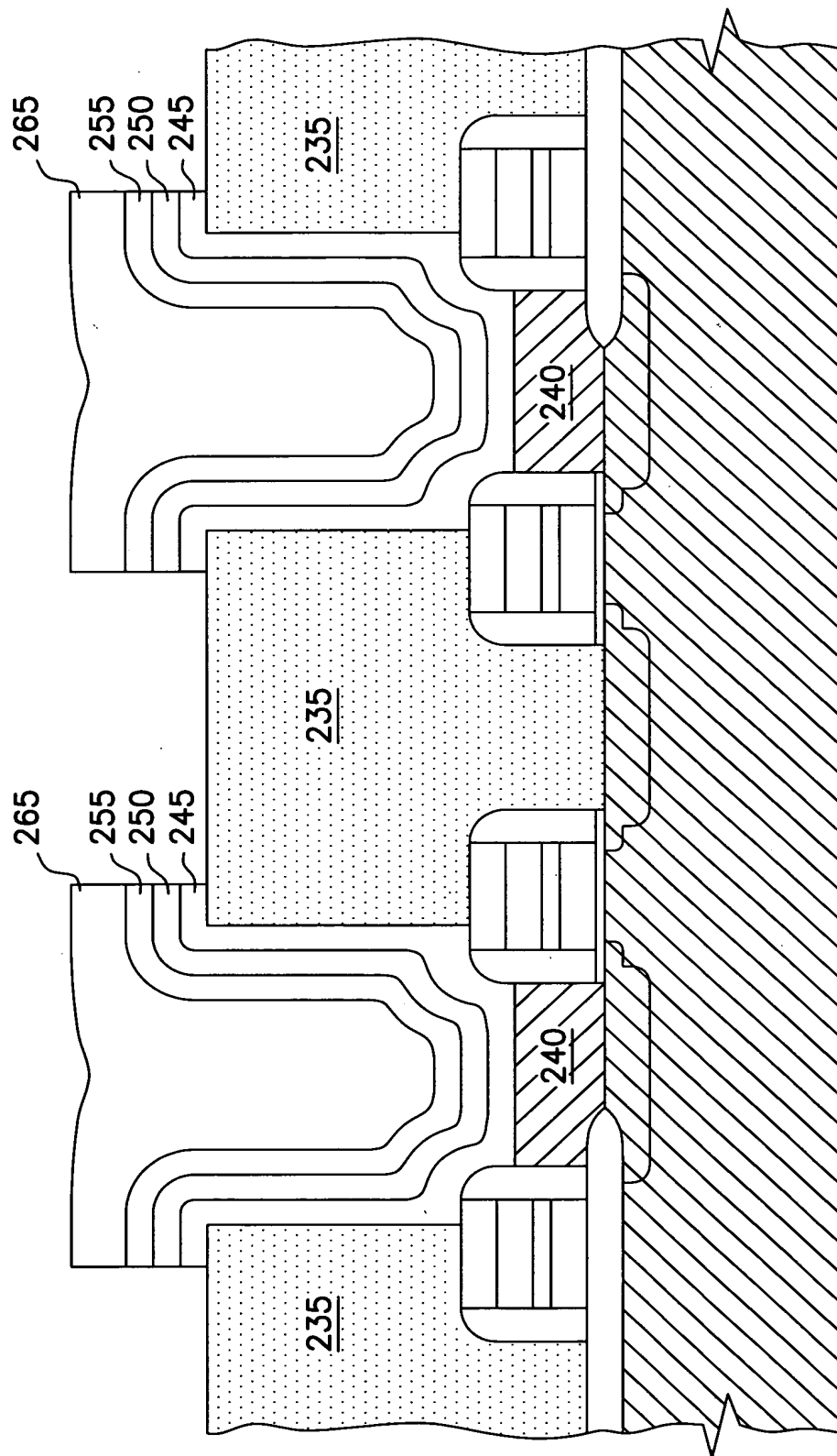


FIG. 21

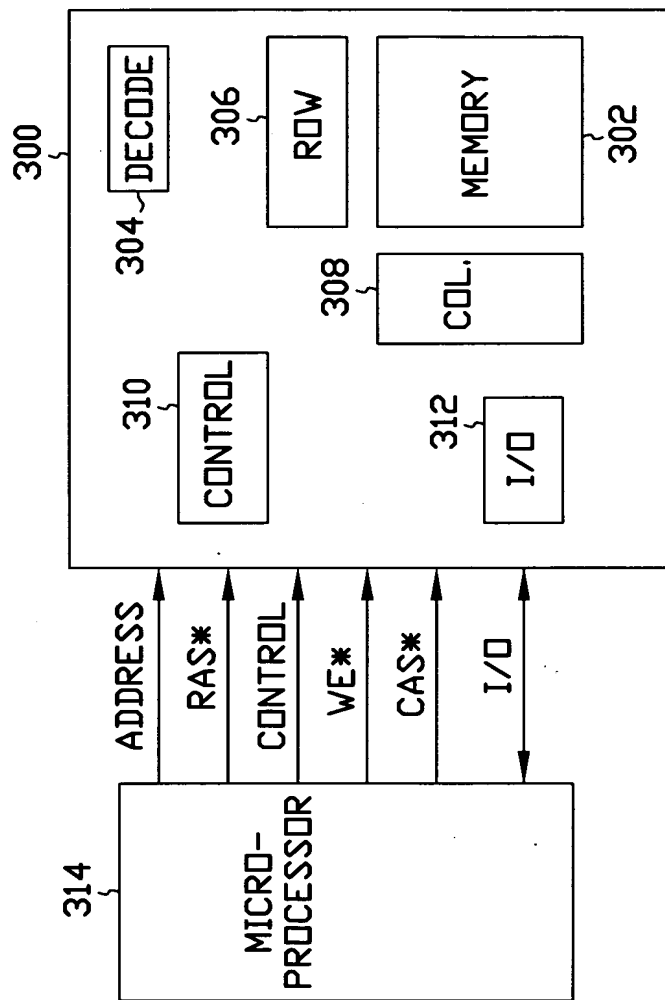


FIG. 3

FIG. 5

Diagram illustrating a multiprogramming system with 700 memory units. A USER I/F (710) is connected to a system containing three 500-unit memory blocks and an ellipsis, totaling 700 units.

FIG. 7

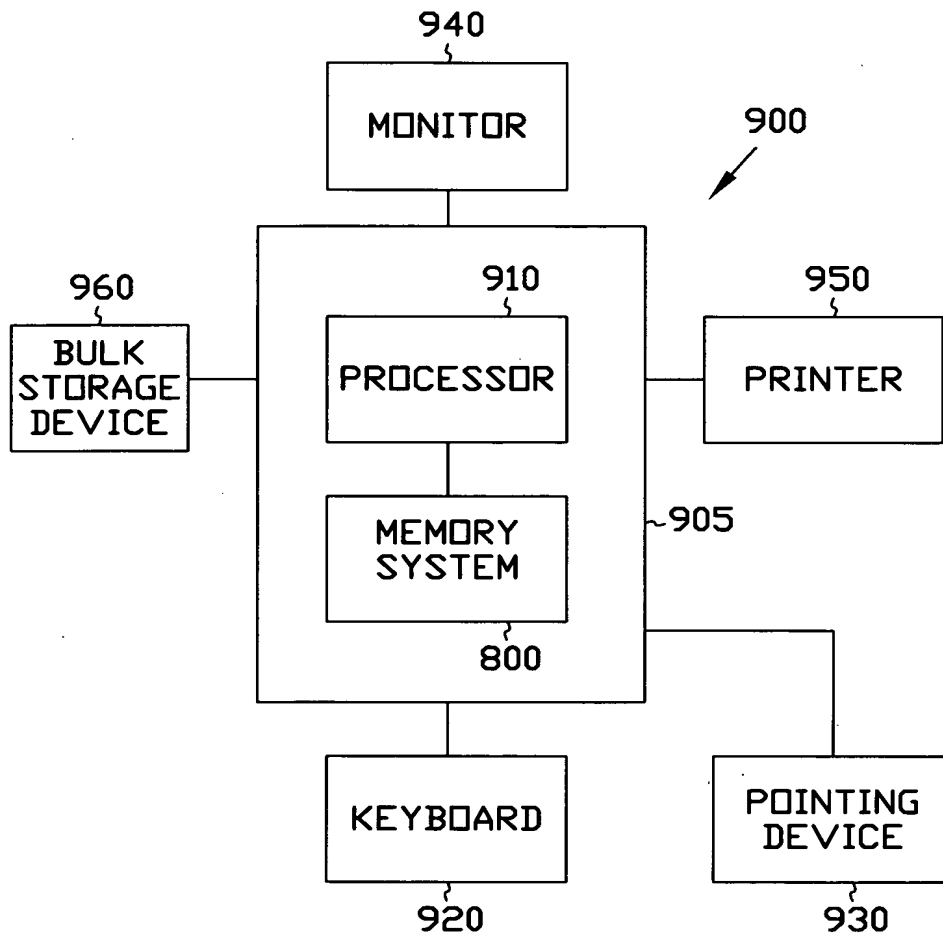
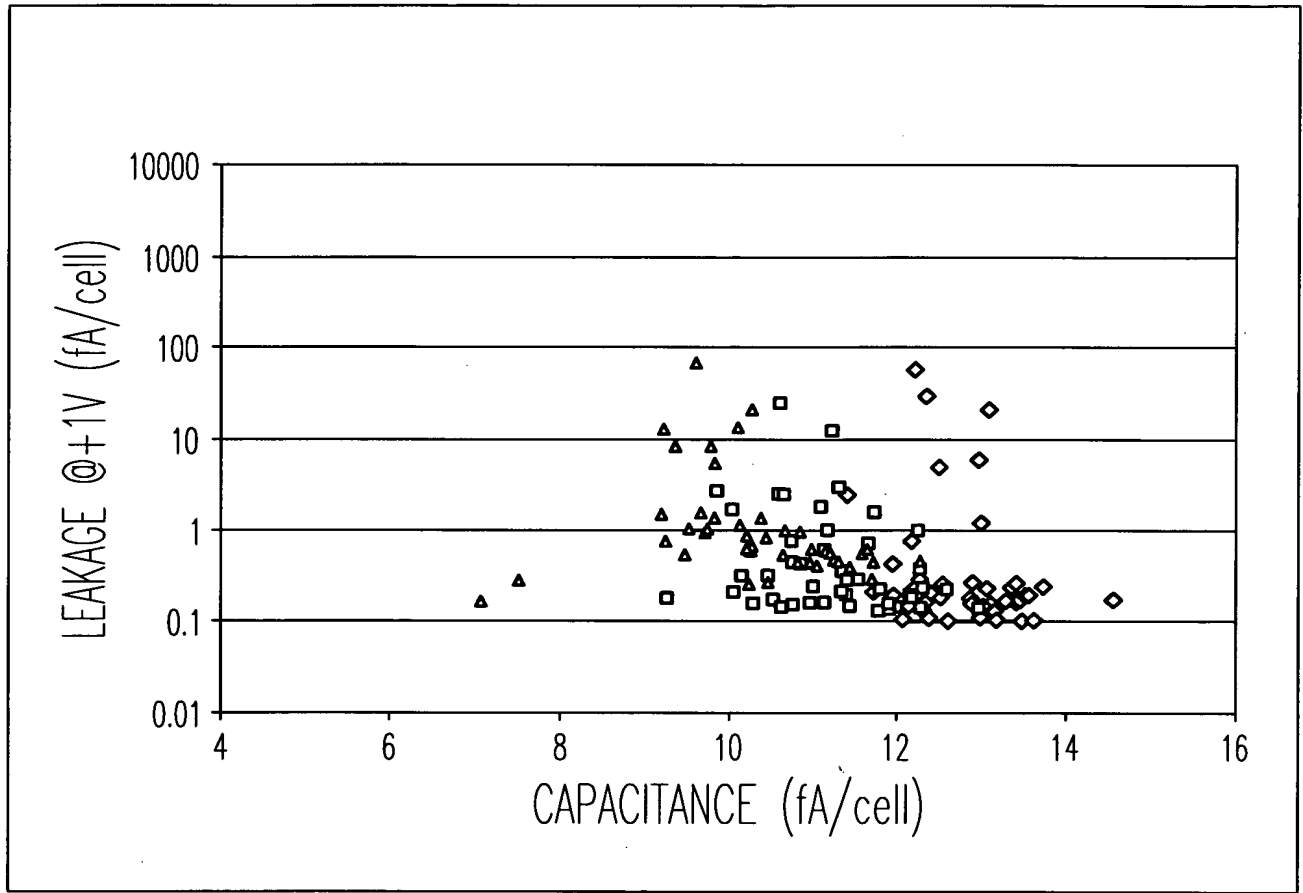


FIG. 9



- ◆ CAPACITORS 3
- CAPACITORS 2
- ▲ CAPACITORS 1

FIG. 10

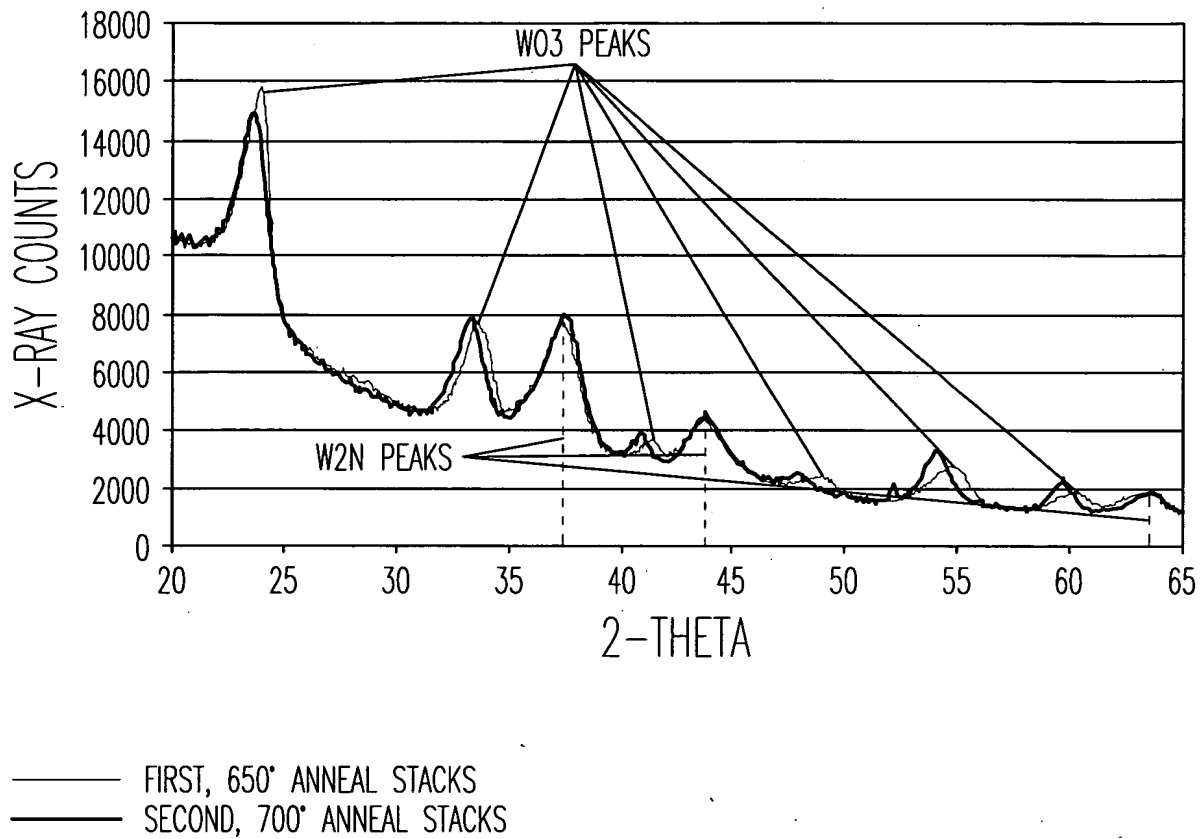
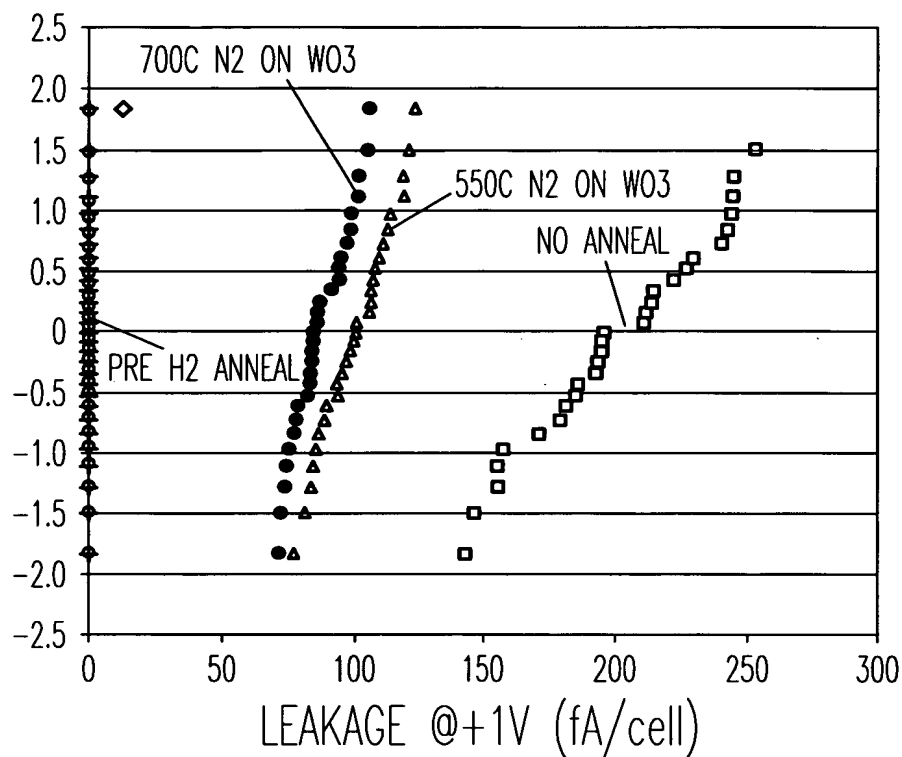


FIG. 11

09745144 122000



- ◇ 1150-02 (NO N2 ANNEAL, AS-FORMED)
- 1150-02 (POST H2 ALLOY)
- + 1150-04 (550C N2 AS-FORMED)
- ▲ 1150-04 (POST H2 ALLOY)
- 1150-08 (750C N2, AS-FORMED)
- 1150-08 (POST H2 ALLOY)

FIG. 12